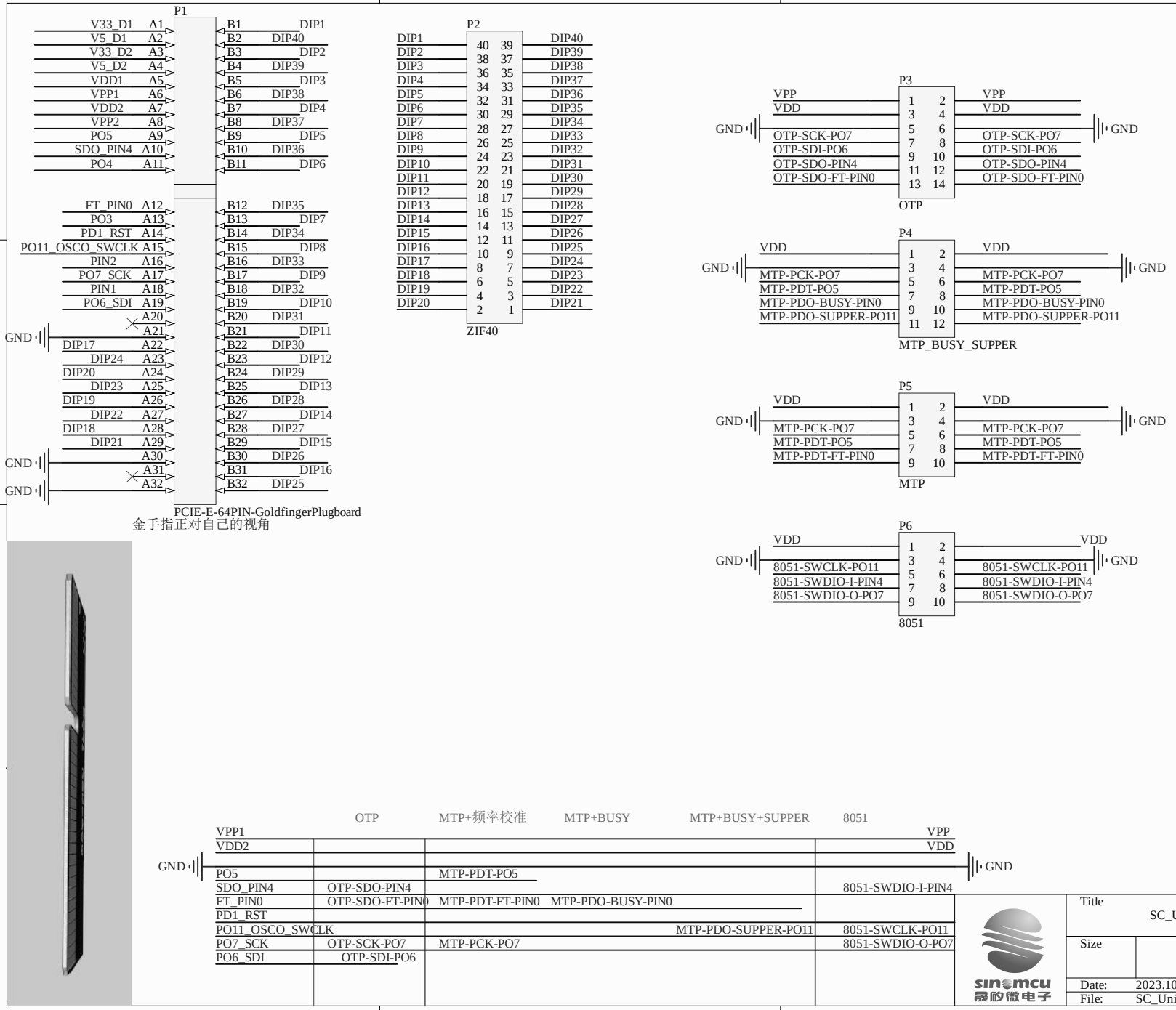
P1
V33_D1 A1
V5_D1 A2
V33_D2 A3
V5_D2 A4
VDD1 A5
VPP1 A6
VDD2 A7
VPP2 A8
PO5 A9
SDO_PIN4 A10
PO4 A11
FT_PIN0 A12
PO3 A13
PD1_RST A14
PO11_OSCO_SWCLK A15
PIN2 A16
PO7_SCK A17
PIN1 A18
PO6_SDI A19
A20
A21
GND
DIP17 A22
DIP24 A23
DIP20 A24
DIP23 A25
DIP19 A26
DIP22 A27
DIP18 A28
DIP21 A29
A30
A31
GND
A32
B1 DIP1
B2 DIP40
B3 DIP2
B4 DIP39
B5 DIP3
B6 DIP38
B7 DIP4
B8 DIP37
B9 DIP5
B10 DIP36
B11 DIP6
B12 DIP35
B13 DIP7
B14 DIP34
B15 DIP8
B16 DIP33
B17 DIP9
B18 DIP32
B19 DIP10
B20 DIP31
B21 DIP11
B22 DIP30
B23 DIP12
B24 DIP29
B25 DIP13
B26 DIP28
B27 DIP14
B28 DIP27
B29 DIP15
B30 DIP26
B31 DIP16
B32 DIP25
PCIE-E-64PIN-GoldfingerPlugboard
金手指正对自己的视角
P2
DIP1 40 39 DIP40
DIP2 38 37 DIP39
DIP3 36 35 DIP38
DIP4 34 33 DIP37
DIP5 32 31 DIP36
DIP6 30 29 DIP35
DIP7 28 27 DIP34
DIP8 26 25 DIP33
DIP9 24 23 DIP32
DIP10 22 21 DIP31
DIP11 20 19 DIP30
DIP12 18 17 DIP29
DIP13 16 15 DIP28
DIP14 14 13 DIP27
DIP15 12 11 DIP26
DIP16 10 9 DIP25
DIP17 8 7 DIP24
DIP18 6 5 DIP23
DIP19 4 3 DIP22
DIP20 2 1 DIP21
ZIF40
P3
VPP 1 2 VPP
VDD 3 4 VDD
GND 5 6 GND
OTP-SCK-PO7 7 8 OTP-SCK-PO7
OTP-SDI-PO6 9 10 OTP-SDI-PO6
OTP-SDO-PIN4 11 12 OTP-SDO-PIN4
OTP-SDO-FT-PIN0 13 14 OTP-SDO-FT-PIN0
OTP
P4
VDD 1 2 VDD
GND 3 4 GND
MTP-PCK-PO7 5 6 MTP-PCK-PO7
MTP-PDT-PO5 7 8 MTP-PDT-PO5
MTP-PDO-BUSY-PIN0 9 10 MTP-PDO-BUSY-PIN0
MTP-PDO-SUPPER-PO11 11 12 MTP-PDO-SUPPER-PO11
MTP_BUSY_SUPPER
P5
VDD 1 2 VDD
GND 3 4 GND
MTP-PCK-PO7 5 6 MTP-PCK-PO7
MTP-PDT-PO5 7 8 MTP-PDT-PO5
MTP-PDT-FT-PIN0 9 10 MTP-PDT-FT-PIN0
MTP
P6
VDD 1 2 VDD
GND 3 4 GND
8051-SWCLK-PO11 5 6 8051-SWCLK-PO11
8051-SWDIO-I-PIN4 7 8 8051-SWDIO-I-PIN4
8051-SWDIO-O-PO7 9 10 8051-SWDIO-O-PO7
8051
OTP
MTP+频率校准
MTP+BUSY
MTP+BUSY+SUPPER
8051
VPP1 VPP
VDD2 VDD
GND GND
PO5 MTP-PDT-PO5
SDO_PIN4 OTP-SDO-PIN4 8051-SWDIO-I-PIN4
FT_PIN0 OTP-SDO-FT-PIN0 MTP-PDT-FT-PIN0 MTP-PDO-BUSY-PIN0
PD1_RST
PO11_OSCO_SWCLK MTP-PDO-SUPPER-PO11 8051-SWCLK-PO11
PO7_SCK OTP-SCK-PO7 MTP-PCK-PO7 8051-SWDIO-O-PO7
PO6_SDI OTP-SDI-PO6
Title
SC_UniAdapter_Manual
Size
Number
*
Revision
V1.0
Date: 2023.10.30
Sheet of *
File: SC_UniAdapter_Manual.SchDoc
Drow By: *
sinomcu
晟矽微电子